Single Molecular Precursor Solution for CuIn(S,Se)$_2$ Thin Films Photovoltaic Cells: Structure and Device Characteristics

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ABSTRACT: A single molecular precursor solution is described for the deposition of CuIn(S,Se)$_2$ (CIS) film onto Mo-coated glass substrates by spin coating, followed by annealing in Se atmosphere. Characterization of the films by X-ray diffraction, Raman spectroscopy and scanning electron microscopy demonstrates the formation of a highly homogeneous and compact 1.1 μm thick CIS layer, with a MoSe$_2$ under-layer. Atomic force microscopy reveals the presence of spherical grains between 400 and 450 nm, featuring surface corrugation in the range of 30 nm. Film composition is found to be in close agreement with that of the precursor solution. Diffuse reflectance spectroscopy shows a direct band gap ($E_g$) of 1.36 eV. Intensity and temperature dependence photoluminescence spectra show characteristic features associated with a donor−acceptor pair recombination mechanism, featuring activation energy of 34 meV. Over 85 solar cell devices with the configuration Mo/CIS/CdS/i-ZnO/Al:ZnO/Ni−Al and an total area of 0.5 cm$^2$ were fabricated and tested. The champion cell shows a power efficiency of 3.4% with an open circuit voltage of 521 mV and short circuit current of 14 mA/cm$^2$ under AM 1.5 illumination and an external quantum efficiency above 60%. Overall variation in each of solar cell parameters remains below 10% of the average value, demonstrating the remarkable homogeneity of this solution processing method. To understand the limitation of devices, the dependence of the open-circuit voltage and impedance spectra upon temperature were analyzed. The data reveal that the CuIn(S,Se)$_2$/CdS interface is the main recombination pathway with an activation energy of 0.79 eV as well as the presence of two “bulk” defect states with activation energies of 37 and 122 meV. We also estimated that the MoSe$_2$ under-layer generates back contact barrier of 195 meV.

KEYWORDS: solar cells, CuIn(S,Se)$_2$, thin films, solution processing, recombination losses, impedance spectroscopy

1. INTRODUCTION

Thin film technology is one of key contributor to global PV, occupying approximately 10% of the installed capacity. In addition to their suitability for flexible and building integrated PV, thin-film devices are characterized by a short energy payback time due to lower material purity and amounts required for efficient light harvesting.¹ Both Cu(In,Ga)(S,Se)$_2$ and CdTe-based technologies continue to show rise in their global module production. ZSW has recently reported 22.6% efficient CIGS solar cell, just surpassing the record efficiency for multicrystalline Si solar cells.² Historically, CIGS evolved out of CuInSe$_2$, where Ga is incorporated to broaden the compositional range of PV active phase and improve device performance.³⁻⁵ The initial success with CuInSe$_2$ and Cu(In,Ga)Se$_2$, led to evolution of their sulpho-selenides and sulfides, that is, CuIn(S,Se)$_2$, CuInS$_2$, and Cu(In,Ga)S$_2$.⁶ Thus, CuIn(S$_{x}$Se$_{1−x}$)$_2$, CIS, and Cu(In,Ga)(S$_{x}$Se$_{1−x}$)$_2$, CIGS (commonly referred to as chalcopyrites), share similar material properties and processing.⁶

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Conventionally, module production employs vacuum-based techniques, such as thermal evaporation or sputtering of metals, followed by reactive annealing under chalcogen atmosphere. While vacuum-based thin film deposition routes continue to be the benchmark and present choice for manufacturing, there has been a recent thrust in evolving toward nonvacuum deposition routes for multicomponent materials for solar cells. Conclusively, module production employs vacuum-based techniques, such as thermal evaporation or sputtering of metals, followed by reactive annealing under chalcogen atmosphere. While vacuum-based thin film deposition routes continue to be the benchmark and present choice for manufacturing, there has been a recent thrust in evolving toward nonvacuum deposition routes for multicomponent materials for solar cells.

2. RESULTS AND DISCUSSION

2.1. Structural and Optical Properties of CuIn(S,Se)₂ Film.

Characteristic X-ray diffraction pattern of CuIn(S,Se)₂ films on Mo coated glass substrate, along with quantitative structure Rietveld refinement is shown in Figure 1. As described in the Experimental Section, films are obtained by spin-coating a solution of chloride salts of Cu and In (1:1) ratio in DMF-IPA in the presence of thiourea, followed by annealing at 500 °C in the presence of Se powder. This procedure was inspired on our recent work on Cu₂ZnSnS₄ thin films. The refinement statistical correlation factors \( R_p \) and \( R_wp \) converge to values of 8.28 and 10.02, respectively, indicating an excellent fit to the model structure. The material is found to crystallize in tetragonal chalcopyrite structure with lattice parameters \( a = 5.6062(2) \text{ Å} \) and \( c = 11.3157(2) \text{ Å} \), corresponding to a tetragonal distortion factor \( (c/a) \) of 1.009. Crystal parameters are in good agreement with that of polycrystalline CuIn(S,Se)₂ samples synthesized by high temperature solid state synthesis. No additional peaks corresponding to crystallographic phases other than chalcopyrite are identified. The Cu-chalcopyte and In-chalcopyte site interatomic distances have been estimated to be 2.383 and 2.490 Å, respectively, while the Cu–S–In bond lengths around S centered tetrahedron varies between 108.98 and 113.38°. The occupancies of Cu site is 0.8824(5), and Cu site in the presence of Se powder. This procedure was inspired on our recent work on Cu₂ZnSnS₄ thin films. The refinement statistical correlation factors \( R_p \) and \( R_wp \) converge to values of 8.28 and 10.02, respectively, indicating an excellent fit to the model structure. The material is found to crystallize in tetragonal chalcopyrite structure with lattice parameters \( a = 5.6062(2) \text{ Å} \) and \( c = 11.3157(2) \text{ Å} \), corresponding to a tetragonal distortion factor \( (c/a) \) of 1.009. Crystal parameters are in good agreement with that of polycrystalline CuIn(S,Se)₂ samples synthesized by high temperature solid state synthesis. No additional peaks corresponding to crystallographic phases other than chalcopyrite are identified. The Cu-chalcopyte and In-chalcopyte site interatomic distances have been estimated to be 2.383 and 2.490 Å, respectively, while the Cu–S–In bond lengths around S centered tetrahedron varies between 108.98 and 113.38°. The occupancies of Cu site is 0.8824(5), and Se on chalcopyte site 0.3773(4), suggest a Cu-poor and S-rich composition. Energy dispersive analysis of X-rays (EDAX) performed over 45 different points across the 5 × 5 cm².
substrate showed average atomic ratios of Cu/In = 0.91 and Se/(S + Se) = 0.36, with a standard deviation of less than 1.6%. These compositions are consistent with the Rietveld analysis, demonstrating the highly homogeneous elemental composition achieved by this method. The broadening of diffraction peak indicates nanocrystalline nature of films, with an average crystallite size in the range of 30 nm as estimated from Debye–Schererr relation.

The Raman spectrum of the CuIn(S,Se)₂ is shown in Figure 1b, in which the various vibrational modes were fitted to Voigt functions. The chalcopyrite structure belongs to D₃d point group with Raman active normal modes given by

\[
\Gamma_{\text{optical}} = A_1 + 3B_1 + 3B_2 + 6E
\]

and

\[
\Gamma_{\text{acoustic}} = B_2 + E
\]

On the basis of the Keating model, the peaks at 329 and 256 cm⁻¹ are assigned to B2 symmetry, while peaks at 293, 217, 198, and 132 are assigned to A₁, E, A₂, and E symmetry, respectively. No other peaks associated with secondary phases were identified. Consequently, XRD and Raman spectra consistently show the formation of phase pure chalcopyrite with slightly Cu deficient and sulfur rich composition.

The film morphology as probed by SEM and AFM is shown in Figure 2. The top-view SEM image (Figure 2a) shows a distribution of grain size ranging from 200 to 1000 nm. The SEM and the AFM images (Figure 2b) show that the surface of the grains is corrugated, with features in the range of 30 nm. A root-mean-square roughness of 60 nm can be estimated over an area of 10 \( \mu \)m. The length scale of the smallest features correlates with the mean crystallite size estimated by XRD. The cross-sectional SEM image in Figure 2c depicts uniform and adherent growth of CIS film across the substrate with a thickness of 1.1 \( \mu \)m over a 500 nm thick layer of Mo. Figure 2d shows an EDAX line scan measured across the red-line highlighted in cross-sectional view. Substantial concentrations of S and Se are seen within the top ~250 nm of the Mo layer suggesting the formation of a Mo(S,Se)₂ film during the annealing step. This layer has strong implications on device functionality which will be discussed in later sections.

Figure 3a shows the diffuse reflectance spectrum of the CIS films between 800 and 1200 nm, featuring a sharp transition in reflectance with an onset at 850 nm. This sharp change in reflectance corresponds to band-to-band transition of the CuIn(S,Se)₂ film. Tauc plot obtained employing the Kubelka–Munk function (Figure S1) allowed estimating a \( E_g = 1.36 \) eV. The dependence of \( E_g \) on \( x \) in CuIn(Sₓ,Se₁₋ₓ)₂ is determined by,

\[
E_g = E_g(\text{Se}) + (E_g(S) - E_g(\text{Se}) - 1.25(1 - x)\Delta \chi)x
\]

where \( \Delta \chi \) is the difference in Phillips electronegativity for Se (1.79) and S (1.89). According to eq 3, a \( E_g = 1.36 \) eV corresponds to a 40% Se composition which is in excellent agreement with values obtained from EDAX and XRD.

Figure 3b displays the power dependent photoluminescence of the CuIn(S,Se)₂ film. The spectra show a broad peak centered at 1.27 eV. The photoluminescence intensity (\( I_{\text{PL}} \)) increases with the excitation power with a power law as indicated in Figure 3b. An exponent below unity along with the blue shift of the peak center with increasing pump power is manifestation of donor–acceptor pair (DAP) radiative recombination. DAP transition energy is smaller than \( E_g \) by an energy factor associated with binding energy and Coulombic interaction of DA centers as described by eq 4

\[
\hbar \nu = E_g - E_\lambda - E_D + \frac{e^2}{4\varepsilon \varepsilon_0 R^2}
\]

where \( \hbar \nu \) is the emitted energy, \( E_\lambda \) is the binding energy for acceptor, \( E_D \) is binding energy for donor, \( R \) is the distance between the donors and acceptors, and \( \varepsilon \) is the relative permittivity. Increasing the incident power generates extra carriers, reducing the distance and Coulombic interaction between the DA centers. In addition, the slightly asymmetrical peak shape and considerable blue shift (6 meV/decade) upon increasing the incident power suggest the presence of electrostatic potential fluctuations. These fluctuations arise from local compositional inhomogeneity in the material and they are responsible for broadening of localized band gap states and band tailing.
The PL maximum shifts to lower energies upon decreasing temperature by a factor of +0.95 meV/K as shown in Figure 3c. While DAP emission typically undergoes a blue shift with decreasing temperature, the red shift observed in these films is a result of the decrease in the ionization energy of deeper DA states due to the thermal ionization of shallower one. This behavior has been reported in the case of Cu-poor CuIn(S,Se)₂ films. The activation energy (E_A,PL) of the DAP emission is obtained by fitting the integrated intensities of peaks versus temperature to a single exponential Arrhenius decay, yielding a value of 34 meV. Again, this value is consistent with previous studies on Cu-poor CuIn(S,Se)₂ (S = 0.7) and CuInSe₂. These observations have been linked to DAP structures involving InCu donor states to VCu acceptor states, which is quite likely in present Cu-poor CIS films.31–33

2.2. Cell Performance. The performance of devices featuring a substrate architecture, that is, glass/Mo/CuInS₂/CdS/i-ZnO/ZnO:Al/Ni–Al architecture with a total area of 0.5 cm² is presented in Figure 4. J–V characteristics of the best device in dark and under AM 1.5G illumination, are shown in Figure 4a. The best cell displays a power conversion efficiency (η) of 3.4% with an open-circuit voltage (V_OC), short-circuit current density (J_SC), and fill factor (FF) of 521 mV, 13.9 mA/cm², and 46.3%, respectively. Variations in key device parameters, V_OC, J_SC, FF, and η, for 82 devices are included in the table inserted in Figure 4a. The data is characterized by a narrow distribution of cell parameters, in particular V_OC demonstrating excellent film homogeneity across the whole substrate area (5 × 5 cm²). It is noticed that the light J–V characteristics fails to comply with the shifting approximation and undergo a crossover under forward bias. Ideality factor (n) ≈ 2 under dark, suggest thermally activated recombination mechanism. The device exhibits a moderate series resistance value which could be linked to the nanoscale corrugation of the film and the presence of a back contact barrier due to formation of Mo(S,Se)₂. The latter could also be responsible for a lower FF.

EQE spectrum of the best device is shown in Figure 4b, featuring a maximum value of 65% at 570 nm. The spectrum shows a steep increase in EQE up to ∼50% at the CIS absorption edge, followed by further rise to 65% between 850 to 650 nm and a sharp fall at the CdS absorption edge at 520
A band gap of 1.35 eV is estimated from the first derivative near the CIS absorption edge, which matches well with values estimated from diffuse reflectance. The EQE also exhibits a weak red response in wavelengths between 850 and 650 nm, which is a well-established behavior arising from a limited diffusion length of carriers and high doping density in the absorber layer. As the wavelength is shifted to shorter values, light penetration depth decreases and carriers are generated closer to the CIS/CdS junction. For a finite carrier diffusion length, the probability of minority carrier reaching the CdS boundary increases, leading to an increase of the EQE value.6

Figure 4c shows the temperature dependence of V_OC, which can be described in terms of

\[ eV_{OC} = E_{A,V_n} - n k_B T \ln \left( \frac{I_{S0}}{I_{SC}} \right) \]  

(5)

where \( E_{A,V_n} \) is activation energy for the recombination, \( n \) is the ideality factor, and \( I_{S0} \) is the temperature dependent prefactor of reverse saturation current.33,34 Neglecting the temperature dependence of \( I_{S0} \), the plot in Figure 4c enables to estimate \( E_{act,V_n} \approx 0.79 \) eV. The fact that \( E_{act,V_n} \) is significantly lower than \( E_g \) and \( n \approx 2 \) suggest surface recombination is the dominant path for carrier losses.33,34 The temperature dependence of \( V_{OC} \) allows establishing the type of recombination present in the device. Given that \( E_{A,V_n} < E_g \) and \( n > 2 \) suggest that surface recombination is the dominant path for carrier losses.33,34 The onset of \( V_{OC} \) saturation at temperatures below 100 K indicates freezing out of the dominant recombination pathway.

Further aspects linking device performance and material properties can be analyzed from the temperature dependence of the impedance spectra as illustrated in Figure 5. The data presented in Bode plots show a decrease in the low frequency impedance amplitude in the range of 10–100 kΩ (Figure 5a), while the frequency dependence of the phase (Figure 5b) indicated the presence of several time constants between 1 MHz and 20 Hz. The spectrum across the whole frequency range could be fitted satisfactorily by adopting the equivalent circuit shown in Figure 5c, featuring a series combination of a \( R_C \) associated with the back CIS/Mo barrier layer and a more complex set of elements associated with the active CIS/CdS layer. On the basis of previous impedance studies on chalcopyrite and CdTe devices,53–37 the second impedance component contains \( R_C \), time constant corresponding to the active CIS/CdS junction, as well as additional elements associated with defect sites connected in parallel. We shall demonstrate further below that \( R_1, R_2, C_1 \), and \( C_2 \) elements described the dynamic behavior of trap states consistent with other independent observations. The continuous line in the Bode plots (Figures 5a and 5b) demonstrate the excellent fits using the equivalent circuit in Figure 5c, with the error converging below 5% across the whole frequency range.

Key parameters associated with the back contact and defect sites extracted from the impedance analysis as a function of temperature are shown in Figure 6. The series resistance \( R_1 \) did not show any discernible temperature dependence and remains around 2.3 Ω cm⁻². The temperature dependence of \( R_2 \) and \( C_2 \) are displayed in the Figure S2, with the latter being consistent with a doping density of the order of 10¹⁵ cm⁻³. \( R_1, R_2 \), and \( R_3 \) show substantial increase with decreasing temperatures (Figure 6a), which can be rationalized in terms of tunneling assisted transport, characteristic of low mobility semiconductors. Figure 6b shows that capacitance associated with defect sites decreases with decreasing temperature. On the basis of these trends, a characteristic frequency associated with the trap state kinetics (estimated as \( \omega_T = 1/R_C \)) can be examined as a function of temperature as shown in Figure 6c. The temperature dependence of \( \omega_T \) can be expressed in terms of

\[ \omega_T = \xi_0 T^2 \exp \left( \frac{E_{A,D}}{k_B T} \right) \]  

(6)

where \( \xi_0 \) is the thermal emission factor and \( E_{A,D} \) is the activation energy of the defect. From the slopes in Figure 6c, it can be estimated activation energies for the defect levels as \( E_{A,D1} = 37.5 \) meV and \( E_{A,D2} = 122 \) meV. The value of \( E_{A,D1} \) is remarkably close to the activation energy donor–acceptor state obtained from the temperature-dependent PL (see Figure 3). This defect level, commonly denoted as \( \alpha \) in the chalcopyrite literature, is generally assigned to acceptors originating from \( V_{ss} \).53,38,39 On the other hand, \( E_{A,D} \) is similar to the value associated with \( N_1 \) states which have been attributed to interface states present at the CIS/CdS interface.38,39 As the potential bias mostly develops at the CIS/CdS junction, the dynamic of electron population at trap states is rather sensitive to the ac potential perturbation.40 It has also been shown that \( N_1 \) states are present even upon replacing CdS by InS₂, suggesting that the defect is associated with the CIS surface.40 In our devices, the highly textured surface of CIS will promote a...
large density of $N_1$ states, increasing carrier shunting paths (moderate shunt resistance) and low $E_{AV}$ values with respect to $E_g$. We could exclude secondary phases and pin-holes as a major contributor to the shunting paths in view of the highly compact and phase pure nature of the films as revealed by SEM, XRD EDAX, and Raman spectroscopy.

The temperature dependence of the back contact resistance at the Mo/CIS junction can be described in terms of \( R_b \) for one given range of $E_{AV}$ and $E_g$ values.

\[
R_b = \frac{k_B}{eA^*T} \exp\left(\frac{\Phi_b}{k_B T}\right)
\]

where $A^*$ is the effective Richardson’s constant and $\Phi_b$ is the back contact barrier height. Plotting $\ln(R_b T)$ versus $T$ (inset in Figure 6c) allows estimating $\Phi_b = 195$ meV.

In summary, the device $J-V$ curves, EQE, and temperature-dependent impedance spectra strongly suggest that defect states present at grain boundaries at the CIS/CdS junction are the key limiting factor in the efficiency of the device, along with the electronic barrier at the Mo/CIS junction. These factors predominantly affect the $J_{SC}$ and FF values. Consequently, it is crucial to design barrier layers that avoid selenization of the Mo substrate during the annealing step. This would enable to tune the annealing conditions in order to decrease grain boundaries at CIS/CdS junction, minimizing surface recombination losses.

3. CONCLUSIONS

We describe a new methodology for processing high quality CuIn(S,Se)$_2$ by spin coating of a single molecular precursor solution based on DMF-IPA mixtures. Deposition and reactive thermal annealing in the presence of Se generate single phase CIS films featuring Cu-poor and S-rich compositions, with a $E_g = 1.36$ eV. Temperature-dependence PL measurements are characterized by a donor–acceptor recombination mechanism with activation energy of 34 meV. Thin film devices with total areas of 0.5 cm$^2$ exhibited efficiencies up to 3.4%. Statistical analysis of the various cell parameters shows a small variation across the whole substrate area ($5 \times 5$ cm$^2$). Temperature dependence impedance spectroscopy in the dark showed characteristic dynamic responses associated with two defect states as well as the interfacial layer generated by selenization of the Mo surface. One of the states exhibited identical activation energy to the radiative recombination dominating the PL response. The activation energies of the defect levels (so-called $\alpha$ and $N_1$ states) are similar to those reported in high quality CIS devices exhibiting efficiencies above 15%.$^{15}$ Consequently, the key limiting factor in our devices is not the nature but potentially the density of these defect states. Considering that some of these states have been linked to grain boundaries, it is crucial to improve the mean grain sizes obtained by our methodology. This involves not only optimization of the annealing step but also passivation of the Mo surface to limit the extent of substrate selenization.

4. EXPERIMENTAL SECTION

The CuIn(S,Se)$_2$ are deposited on to $5 \times 5$ cm$^2$ Mo-coated glass substrate (MSoliv) using a spray approach similar to processing used for Cu$_2$ZnSnS$_4$ films and devices reported previously.$^{22}$ No blocking layer between Mo and glass is employed in order to minimize Na diffusion from substrates to absorber. CIS films are deposited by spin-coating a DMF-IPA (1:1) solution of Cu(II) and In(III) chloride (with an equimolar concentration of 1.6 M), as well as thiourea at 2000 rpm, followed by heating at 300 °C in air for 2 min. This step is repeated 4 times to achieve a final thickness slightly larger than 1 μm. The films are subsequently placed in a graphite box with 100 mg of selenium powder for annealing. Annealing is performed in a MTI-OTF1200X furnace with a ramping of 10 °C/s to 500 °C, dwelling at 500 °C for 30 min and cooling naturally, exhausting all the selenium. Prior to the buffer layer deposition, the as-annealed films are etched in a 10% KCN (aq.) solution. Immediately after annealing, the CIS layer is grown by chemical bath deposition employing CdS$_{0.5}$thiourea and ammonium hydroxide, at 70 °C. The devices are completed by RF-sputtering an i-ZnO (50 nm) and Al-doped ZnO window layer (350 nm), followed by evaporation of Ni/Al contact grid using a shadow mask. Total area of a cell is defined by mechanically scribing a 0.5 cm $\times$ 1.0 cm rectangular area from an array of 32 cells on 5 cm $\times$ 5 cm support. The shadowing generated by the top metallic contact is not considered in the measurements and no antireflection coating was used. $J-V$ plots were measured in dark and under illumination with simulated AM 1.5 G spectrum (in-house class A solar simulator) and integrated power density of 100 mW/cm$^2$ at 23 °C. The solar simulator has a homogeneous and collimated illumination area of 17.5 cm $\times$ 17.5 cm and a temperature controlled area of 11.5 cm $\times$ 11.5 cm in the center. The measurements are performed by placing the sample in the center.
The illumination divergence over such small area of cell with an area of 0.5 cm² is considered to be negligible. A total of 125 points were measured by scanning the voltage from 0.75 to −0.5 V, with no evidence of hysteresis observed (as expected for this fully inorganic solid-state device). The external quantum efficiency (EQE) was recorded employing halogen and Xe lamps with a Bentham TM 300 monochromator (Bentham instruments). ISE-Fraunhofer certified Cd and Ge cells are used as the reference for both solid-state devices. The external quantum efficiency (EQE) was measured by scanning the voltage from 0.75 to −0.5 V and EQE measurements. The mismatch factor between the tested and reference cell was found to be 0.9756. Low-temperature impedance measurements are carried out under dark using a Solartron impedance analyzer, interfaced with Linkam HFS 600PB4 cooling stage, in frequency range of 20 Hz to 1 MHz with no applied DC bias and AC stimulus of 25 mV.

### Associated Content

#### Supporting Information

The Supporting Information is available free of charge on the ACS Publications website at DOI: 10.1021/acsami.6b12306.

Film characterization details, modified Tauc plot from diffuse reflectance for estimating band gap, and temperature variation of CIS/CdS capacitance, \( C_v \), and resistance, \( R_j \).

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**Notes**

The authors declare no competing financial interest.

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### References


