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Error Performance of circular 16-DAPSK in Frequency-Selective Rayleigh Fading Channels with Diversity Reception

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Abstract: An expression for the error probability of raised cosine filtered circular 16-ary differential amplitude and phase shift keying (circular 16-DAPSK) in a frequency-selective Rayleigh fading channel with postdetection diversity reception is obtained. The effect of delay spread, modulation ring ratio and filter roll-off are also considered in the formulation. For a constant bit rate, the average irreducible bit error probability for circular 16-DAPSK is shown to be superior to that of 16-DPSK but slightly worse than Q-DPSK.

Introduction

Future mobile networks are looking to integrate digital voice with high capacity data based services. Digital modems are prone to the introduction of an irreducible error rate that arises as a result of user motion and/or time dispersion in the channel [1]. The resulting error floors are independent of signal strength and, unless corrected, can limit either the speed of the mobile (Doppler) or the maximum transmission rate (time dispersion). Hence, for a high data rate wireless modem, the effect of delay spread must be carefully considered.

The effect of delay spread on bandwidth efficient modulations schemes has already been investigated by various authors. The error performance of quadrature coherent PSK with bandlimiting raised cosine (RC) pulse shaping was examined in [2],[3]. Both papers concluded that Q-PSK outperform B-PSK under the same channel conditions. An indepth treatment of the error performance of RC filtered Q-DPSK with postdetection diversity combining is covered in [4]. The error performance of M-ary DPSK in frequency selective channels has already been analysed including the effects of Doppler shift [5], where diversity combining was considered with time-limited pulse shapes. Recently, work in [6] has formulated the error performance of RC filtered M-DPSK in a frequency-selective Rayleigh fading channel with diversity reception. The irreducible bit error probability of B-DPSK is worse than higher level schemes such as 4, 8 and 16-DPSK. No improvement is observed when the modulation level is greater than four. This trend arises since the impact of a more complex, higher level, constellation pattern can override the advantages of a longer symbol period. The error performance of PSK and APSK (amplitude and phase shift keying) in a frequency-selective Rayleigh fading channel has also been reported in earlier works. In [7], 16-QAM was compared with M-PSK assuming coherent detection. In [8], circular 16-DAPSK was compared with M-DPSK. Both works used RC pulse shaping, however diversity reception was not considered in the comparison. Recently, in [9], the authors have calculated the error probability of circular 16-DAPSK in a frequency-selective Rayleigh fading channel with postdetection diversity reception and time-limited pulse shapes. The results have shown that circular 16-DAPSK performs better when compared with 16-DPSK and the improvement increases when diversity is applied. However, in a practical situation, a bandlimited signal is normally used to improve bandwidth efficiency in a mobile system. Therefore, to justify the advantage of circular 16-DAPSK over 16-DPSK, it is essential to analysis the error performance of circular 16-DAPSK with bandlimited filtering.

The aim of this paper is to extend the work in [9], so that the error probability can be calculated when bandlimited pulse shapes are applied to circular 16-DAPSK in a frequency-selective Rayleigh channel. The expression takes into account factors such as root mean square (rms) delay spread, ring ratio, raised cosine roll-off factor and post-detection diversity combining. Using such equations, the irreducible error floor introduced by delay spread can be evaluated and the sensitivity to the filter roll-off value determined both with and without the use of diversity combining.

System Modelling

The overall transmission system with postdetection diversity reception is modelled as shown in Fig.1. For mathematical convenience, an equivalent lowpass signal repre-
sentation is used. To achieve both a narrowband spectrum and intersymbol interference (ISI) free transmission in the absence of delay spread, the overall raised cosine filter response is applied. [2]-[4]:

\[ h_{rc}(t) = \frac{1}{T_s} \sin(t/T_s) \cos(\alpha \pi t/T_s) \]

where \( \alpha \) (0 \( \leq \alpha \leq 1 \)) is the roll-off factor and \( T_s \) is the symbol duration. In this paper, a square root RC filter is assumed for both transmitter and receiver, and hence their frequency responses can be written as:

\[ H_r(f) = \sqrt{H_{rc}(f)} \]

For circular 16-DAPSK transmission, the equivalent low-pass transmitted signal \( u(t) \) can be written as:

\[ u(t) = A \sum_{n=-\infty}^{\infty} d_n e^{j\theta_n} h_r(t-nT_s) = \sum_{n=-\infty}^{\infty} u_n \]

where \( d_n = 1 \) or \( \beta \), and \( \beta \) represents the ring ratio. \( A d_n \) represents the amplitude of the transmitted signal and \( \theta_n = \pi(i-1)/4 \), where \( i = 1, 2, \ldots, 8 \) is the transmitted phase. The time response of the transmit filter is defined as \( h_r(t) \). The differential encoding technique for circular 16-DAPSK is discussed in [10]. The signal \( r_k(t) \) is passed through a matched filter with an impulse response of \( h_r(t) \) and sampled at time \( t = pT_s \), where \( p \) is an integer number. The output of the filter is therefore:

\[ U_k = \int_{-\infty}^{\infty} r_k(\alpha) h_r(t-\alpha) d\alpha \]

and similarly, for \( r_k(t-T_s) \), the output of the filter is:

\[ K_k = \int_{-\infty}^{\infty} r_k(\alpha-T_s) h_r(t-\alpha) d\alpha \]

For amplitude detection, a square-law envelope detector is used to extract the envelope of \( U_k \) and \( K_k \). The combined outputs after the postdetection combiner, \( |U|^2 = \sum_{k=1}^{L} |U_k|^2 \) and \( |K|^2 = \sum_{k=1}^{L} |K_k|^2 \), are then passed to the amplitude decision device, where the amplitude ratio \( |U|^2/|K|^2 \) is decoded by comparing it with two decision thresholds, \( \xi_u = (1+\beta)/2 \) and \( \xi_k = 1/\xi_u \). For the phase detection, a conventional 8-DPSK detector is applied. As indicated in Fig.1, the demodulator at each branch forms the product between the two complex Gaussian random variables of (6) and (7), so for \( L \) order diversity, the combiner sums all the demodulator outputs and forms a combined vector, \( Z = \sum_{k=1}^{L} \phi_k = \sum_{k=1}^{L} U_k K_k^{*} \).

Mathematical Analysis

The error performance of circular 16-DAPSK in frequency-flat fading channels has already been analysed in [12],[13],[14]. The error probability can be calculated using two consecutive signalling periods. However, in a frequency-selective fading channel, due to delay spread, the error is dominated by the time varying ISI, hence the error performance depends on the transmitted sequence, \( \Phi \):

\[ \Phi = (\ldots, u_{n-2}, u_{n-1}, u_n, u_{n+1}, u_{n+2}, \ldots) \]

The probability of detecting the amplitude and phase of a data symbol incorrectly can be calculated by using the expressions below:

\[ P_a = \frac{1}{N_{\Phi}} \sum_{\Phi} P_a(\Phi, d_{n-1}, d_n) \]

\[ P_p = \frac{1}{N_{\Phi}} \sum_{\Phi} P_p(\Phi, \theta_{n-1}, \theta_n) \]
where \( N_p \) is the number of possible sequences. 
\( P_a(\Phi, d_{n-1}, d_n) \) is the conditional probability of making an amplitude decision error when the transmitted amplitude sequence goes from \( d_{n-1} \) to \( d_n \). 
\( P_p(\Phi, \theta_{n-1}, \theta_n) \) is the conditional probability of making a phase error when the transmitted phase goes from \( \theta_{n-1} \) to \( \theta_n \). The expressions for the conditional amplitude and phase error probabilities are already given in [9]. Both expressions are a function of the second central moments between the matched filter outputs, \( U_k \) and \( K_k \). The second central moments \( m_{UU} \), \( m_{KK} \) and \( m_{UK} \) (normalised to \( T_s/2N_o \)) are shown below:

\[
\begin{align*}
    m_{UU} &= \Gamma_s \int_{-\infty}^{\infty} \xi_g(0, \tau) \left\{ \sum_{n=-\infty}^{\infty} d_n e^{j\Phi_n} H_n \right\} \times \left\{ \sum_{n=-\infty}^{\infty} d_n e^{-j\Phi_n} H_n \right\} d\tau + 1 \\
    m_{KK} &= \Gamma_s \int_{-\infty}^{\infty} \xi_g(0, \tau) \left\{ \sum_{n=-\infty}^{\infty} d_n e^{-j\Phi_n} H_n \right\} \times \left\{ \sum_{n=-\infty}^{\infty} d_{n-1} e^{-j\Phi_{n-1}} H_{n-1} \right\} d\tau + 1 \\
    m_{UK} &= \Gamma_s \int_{-\infty}^{\infty} \xi_g(T_s, \tau) \left\{ \sum_{n=-\infty}^{\infty} d_n e^{j\Phi_n} H_n \right\} \times \left\{ \sum_{n=-\infty}^{\infty} d_{n-1} e^{-j\Phi_{n-1}} H_{n-1} \right\} d\tau + 1
\end{align*}
\]  

(11) (12) (13)

where \( H_n = T_s h_{rc}(\mu T_s - \tau - nT_s) \) and \( \Gamma_s = (1 + \beta)A^2 T_s/4N_o \). \( \Gamma_s \) represents the average received symbol energy-to-noise power spectral density ratio per channel.

**Numerical Results and Discussion**

Analysis in the last section has shown that the calculated error performance is dependent on the number of interference symbols considered in equations (11)-(13). Although the equations derived in the last section allow us to consider any number of interference symbols, in this paper only adjacent-pulse-limited ISI channels [5],[6],[11], are considered for numerical evaluation. Without loss of generality, the symbol at time \( n = 0 \) is used for detection, hence the sampling time, \( pT_s = 0 \). The average amplitude and phase error probabilities for circular 16-DAPSK can be written in the following form:

\[
\begin{align*}
    P_a &= \frac{1}{24k} \sum_{\Phi} P_a(\Phi, d_{-1}, d_0) \\
    P_p &= \frac{1}{24k} \sum_{\Phi} P_p(\Phi, \theta_{-1}, \theta_0)
\end{align*}
\]  

(14) (15)

where \( k = 4 \) represents the number of bits per symbol and \( \Phi = (u_{-2}, u_{-1}, u_0, u_1) \) represents the truncated transmitted sequence. (Note: For expressions \( P_a(\Phi, d_{-1}, d_0) \) and \( P_p(\Phi, \theta_{-1}, \theta_0) \) see [9, Eq.2 and 9]). The second central moments are simplified to:

\[
\begin{align*}
    m_{UU} &= \Gamma_s \int_{-\infty}^{\infty} \xi_g(0, \tau) D_{UU} d\tau + 1 \\
    m_{KK} &= \Gamma_s \int_{-\infty}^{\infty} \xi_g(0, \tau) D_{KK} d\tau + 1 \\
    m_{UK} &= \Gamma_s \int_{-\infty}^{\infty} \xi_g(T_s, \tau) D_{UK} d\tau
\end{align*}
\]  

(16) (17) (18)

with

\[
\begin{align*}
    D_{UU} &= d^2_0 H_2^2 + d^2_1 H_3^2 + d^2_2 H_4^2 \\
    &+ 2\left[ d_{-1} d_0 H_{-1} H_0 \cos(\theta_0 - \theta_{-1}) \right. \\
    &+ d_{-1} d_1 H_{-1} H_1 \cos(\theta_1 - \theta_{-1}) + d_0 d_1 H_0 H_1 \cos(\theta_1 - \theta_0) \left. \right] \\
    D_{KK} &= d^2_0 H_2^2 + d^2_1 H_3^2 + d^2_2 H_4^2 \\
    &+ 2\left[ d_{-2} d_{-1} d_1 H_{-1} H_0 \cos(\theta_0 - \theta_{-2}) \right. \\
    &+ d_{-2} d_0 d_1 H_{-1} H_1 \cos(\theta_1 - \theta_{-2}) + d_{-1} d_0 d_1 H_0 H_1 \cos(\theta_2 - \theta_{-1}) \left. \right] \\
    D_{UK} &= d_{-2} d_{-1} d_1 H_{-1}^2 e^{j(\theta_{-1} - \theta_{-2})} + d_{-2} d_0 H_0^2 e^{j(\theta_0 - \theta_{-1})} \\
    &+ d_{-1} d_1 H_0^2 e^{j(\theta_0 - \theta_{-1})} + d_0 d_1 H_1^2 e^{j(\theta_0 - \theta_{-2})} \\
    &+ d_{-1} d_0 H_{-1}^2 H_0^2 e^{j(\theta_0 - \theta_{-2})} + d_{-1} d_0 d_1 H_0 H_1 e^{j(\theta_1 - \theta_{-2})}
\end{align*}
\]

Assuming \( \xi_g(\tau) \) is defined with a rectangular power delay profile as in [11], the average bit error probability for 4-, 16-DPSK and circular 16-DAPSK with 1st and 2nd order diversity is shown in Fig.2 as a function of normalised rms delay spread, \( d_b \) (a roll-off factor of 0.5 is used). The rms delay spread is normalised in terms of the bit period (i.e. \( d_b = \tau_{rms}/T_b \)) so that the error performances can be compared for the same information throughput for different levels of modulation scheme. The result shows that when the system operates without diversity, the performance of circular 16-DAPSK is close to that of QDPSK. The effect of filter roll-off factor, \( \alpha \), on the error probability is shown in Fig.3 for \( d_b = 0.2 \). As expected, the error probability is reduced as \( \alpha \) is increased. For circular 16-DAPSK, the modulation ring ratio, \( \beta \), has been optimised for the above results. A value of 2.2 was found to result in the best performance. The numerical results shown in Fig.2 and 3 have indicated that circular 16-DAPSK outperforms 16-DPSK when bandlimited pulse shaping is applied to the systems. This trend confirms the result given in [9] where simpler time-limited pulse shapes were used. The advantage of using circular 16-DAPSK rather than 16-DPSK is further enhanced when postdetection diversity reception is applied. For an rms delay spread of 50ns (indoor environments) and a target bit error probability of 1e-5, assuming second order diversity reception,
a QDPSK modem can transmit up to 1.2Mbps whereas for 16-DPSK only 800kbps can be achieved. Using circular 16-DAPSK, the maximum allowable transmission rate is 1.04Mbps. For outdoor applications, a 64kbps data rate and a target bit error probability of 1e-3 were assumed. Once again, assuming second order diversity reception, QDPSK and 16-DPSK can tolerate rms delay spreads of 3.125μs and 1.875μs respectively. Applying circular 16-DAPSK, an rms delay spread of 2.66μs can be tolerated.

Conclusions
The theoretical error performance of raised cosine filtered circular 16-DAPSK with postdetection diversity combining has been analysed for a frequency-selective Rayleigh fading channel. The numerical results have shown that although the error probability of circular 16-DAPSK is slightly worse when compared with Q-DPSK under the same bit rate transmission (2nd order diversity), it is superior when compared to 16-DPSK. The advantage of circular 16-DAPSK over 16-DPSK is enhanced when post-detection diversity reception is applied. Although circular 16-DAPSK and 16-DPSK both operate with symbol periods twice that of Q-DPSK, it is interesting to note that the error performance of these schemes are not significantly improved. However, for the same bit rate, both 16-level schemes use half the bandwidth occupied by Q-DPSK.

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References
Fig. 1: Block diagram for circular 16-DAPSK transmission system in equivalent lowpass representation.

Fig. 2: Average irreducible bit error probability of 4, 16-DPSK and circular 16-DAPSK versus normalised rms delay spread, $d_b$. Rectangular delay profile and $\alpha = 0.5$.

Fig. 3: Average irreducible bit error probability of 4, 16-DPSK and circular 16-DAPSK versus raised cosine filter roll-off. Rectangular delay profile and $d_b = 0.2$. 